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DECISION FEEDBACK EQUALIZER AND PRECODER RAMPING CIRCUIT

ABSTRACT OF THE DISCLOSURE

A feedforward filter has a plurality of feedforward filter taps, including a feedforward filter reference tap. The reference tap of the feedforward filter is positioned proximate a center position of the feedforward filter. A ramping circuit assembly has an input port configured to receive at least one decision feedback filter tap coefficient from a decision feedback filter. A coefficient ramping circuit is configured to provide a ramped output for at least one of the decision feedback filter tap coefficients. The ramped output is varied over time from a first value to a second value. The second value is dependent upon the value of a decision feedback filter tap coefficient. An output port of the ramping circuit assembly is configured to communicate information representative of the ramped output(s) to a precoder.

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